

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Alan David Bennett et al.  
Title: Non-Volatile Memory and Method With Block Management System  
Application No.: 10/750,155 Filing Date: December 30, 2003  
Examiner: Unknown Group Art Unit: 2818  
Docket No.: SNDK.343US0 Conf. No.: 7902

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Certificate of Mailing Under 37 CFR 1.8

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Mary E. Buggie  
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Commissioner for Patents  
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Alexandria, VA 22313-1450

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

Dear Sir:

Pursuant to 37 C.F.R. §§ 1.56, 1.97 and 1.98, Applicants call the documents listed on the enclosed Form PTO-1449 to the Examiner's attention in this patent application.

According to 37 C.F.R. 1.98(2)(ii), copies of the U.S. Patents and U.S. Published Patent Applications documents are not required and are therefore not enclosed. Copies of the listed foreign patent documents and Other Art are enclosed.

Citation of these documents shall not be construed as (1) an admission that the documents are prior art with respect to the invention or inventions claimed in this application, (2) a

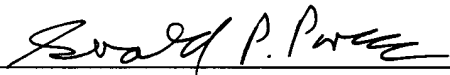
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representation that a search has been made (other than as indicated by any cited document), or  
(3) an admission that the cited information is, or is considered to be, material to patentability as  
defined in § 1.56(b).

This information disclosure statement is submitted under 37 C.F.R. § 1.97(b) and  
consequently no fee should be required. The Commissioner is authorized, however, to charge  
any fee that may be required, or to credit any overpayment, against Deposit Account No.  
502664. This form is being submitted in duplicate.

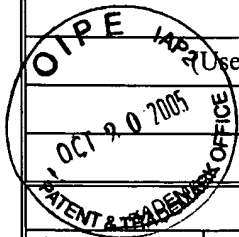
Respectfully submitted,

  
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U.S. Department of Commerce, Patent and Trademark		Atty. Docket No.	Application No.
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		Applicants	Conf. No.
Use several sheets if necessary)		Bennett et al.	7902
(Form PTO-1449)		Filing Date	Art Group
		December 30, 2003	2818



### U.S. Patent Documents

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	1	5,043,940	8/1991	Harari			
	2	5,172,338	12/1992	Mehrotra et al.			
	3	5,404,485	4/1995	Ban			
	4	5,532,962	7/1996	Auclair et al.			
	5	5,774,397	6/1998	Endoh et al.			
	6	5,798,968	8/1998	Lee et al.			
	7	5,890,192	3/1999	Lee et al.			
	8	5,907,856	5/1999	Estakhri et al.			
	9	5,909,449	6/1999	So et al.			
	10	5,930,167	7/1999	Lee et al.			
	11	5,937,425	8/1999	Ban			
	12	6,034,897	3/2000	Estakhri et al.			
	13	6,125,435	9/2000	Estakhri et al.			
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	15	6,377,500	4/2002	Fujimoto et al.			
	16	6,426,893	7/2002	Conley et al.			
	17	6,456,528	9/2002	Chen			
	18	6,490,649	12/2002	Sinclair			
	19	6,522,580	2/2003	Chen et al.			
	20	6,563,734	5/2003	Taki			
	21	6,591,330	7/2003	Lasser			
	22	6,725,321	4/2004	Sinclair et al.			
	23	6,763,424	7/2004	Conley			

### U.S. Published Patent Application Documents

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	24	2003/0065899 A1	4/2003	Gorobets			
	25	2003/0109093 A1	6/2003	Harari et al.			
	26	2005/0144358 A1	6/2005	Conley et al.			
	27	2005/0141313 A1	6/2005	Gorobets et al.			

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<b>U.S. Published Patent Application Documents (continued)</b>								
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate	
	28	2005/0141312A1	6/2005	Sinclair et al.				
	29	2005/0166087A1	7/2005	Gorobets				
	30	2005/0144365A1	6/2005	Gorobets et al.				
	31	2005/0144357A1	6/2005	Sinclair				
	32	2005/0144363A1	6/2005	Sinclair				
	33	2005/0144367A1	6/2005	Sinclair				
<b>Foreign Patent Documents</b>								
							Translation	
		Document	Date	Country	Class	Subclass	Yes	No
	34	JP 5314019	11/1993	Japan			Abstract	X
	35	WO 02/058074A2	7/2002	WIPO			X	
	36	WO 03/029951A2	4/2003	WIPO			X	
	37	EP 1424631 A1	6/2004	Europe			X	
	38	WO2004/040459A1	5/2004	WIPO			X	
	39	WO2004/040578A1	5/2004	WIPO			X	
	40	WO2004/040458A1	5/2004	WIPO			X	
	41	WO2004/040457A1	5/2004	WIPO			X	
<b>OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)</b>								
	42	Bennett et al., "Scratch Pad Block", U.S. Application Serial No. 11/016,285, filed 12/16/2004, 55 pages.						
	43	Chang, Li-Pin et al., "An Adaptive Striping Architecture for Flash Memory Storage Systems of Embedded Systems", Proceedings of the Eighth IEEE Real-Time and Embedded Technology and Applications Symposium, September 24, 2002, pp. 187-196.						
	44	EPO/ISA, "Notification of Transmittal of the International Search Report and the Written Opinion of the International Searching Authority, or the Declaration", mailed in related PCT/US2004/043597 on August 8, 2005, 12 pages.						
	45	EPO/ISA, "Notification of Transmittal of the International Search Report and the Written Opinion of the International Searching Authority, or the Declaration", mailed in related PCT/US2004/043680 on September 23, 2005, 18 pages.						
Examiner			Date Considered					
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.								